

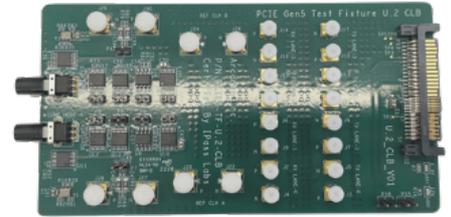


PCI Express Gen 5 Test Fixture Introduction

PCIe Gen 5 U.2

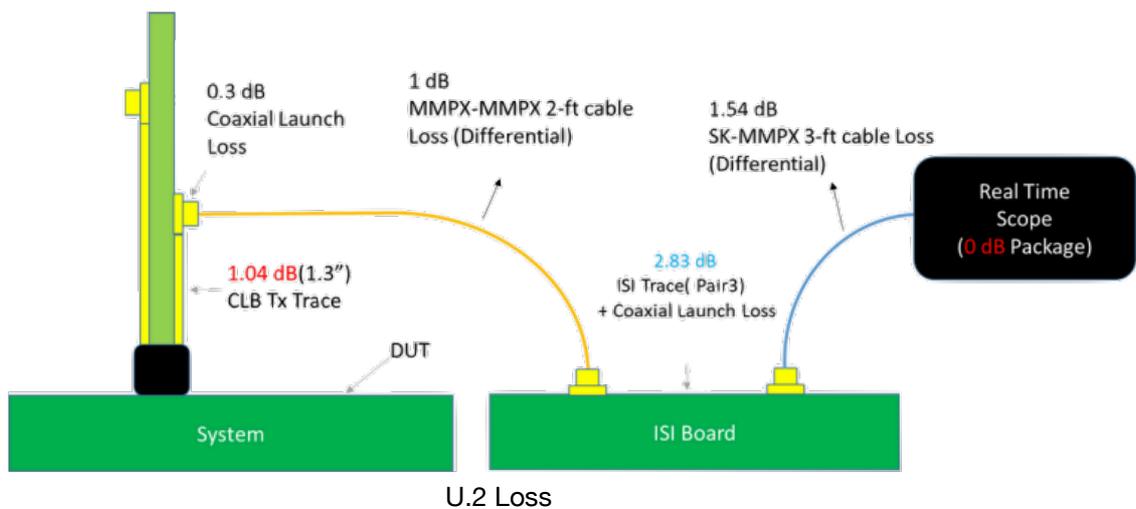
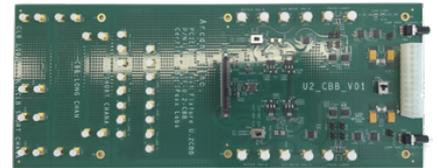
CLB

- Signal Integrity Board for U.2(SFF-8693) Physical Layer Conformance.
- Support PCIe5.0 (32GT/s) used for testing Transmitters.
- Paired with U.2 Gen5 (CBB) used for Receiver Calibration.
- Comes with one (1) CLB. PCIE-TF-U.2-CLB.
- Need additional Gen5 CEM (SIG-REV4) ISI Board for the testing.
- MMPX connector is 100% compatible with PCI-SIG CEM ISI board.



CBB

- Signal Integrity Board for U.2(SFF-8693) Physical Layer Conformance.
- Support PCIe5.0 (32GT/s) used for testing Receivers.
- Paired with U.2 Gen5 (CLB) used for Receiver Calibration.
- Comes with one (1) CBB. PCIE5-TF-U.2- CBB.
- MMPX connector is 100% compatible with PCI-SIG CEM ISI board.



	CLB Tx Trace Loss	Coaxial Launch Loss(Pair)	MMPX-MMPX 2-ft Cable Loss	ISI Trace Loss	SK-MMPX 3-ft Cable Loss	Package Loss	Total Loss (Target: 6.5 dB)
Actual	1.04dB	0.3dB	1dB	2.83dB	1.54dB	0dB	6.71dB

PCIe Gen 5 U.3

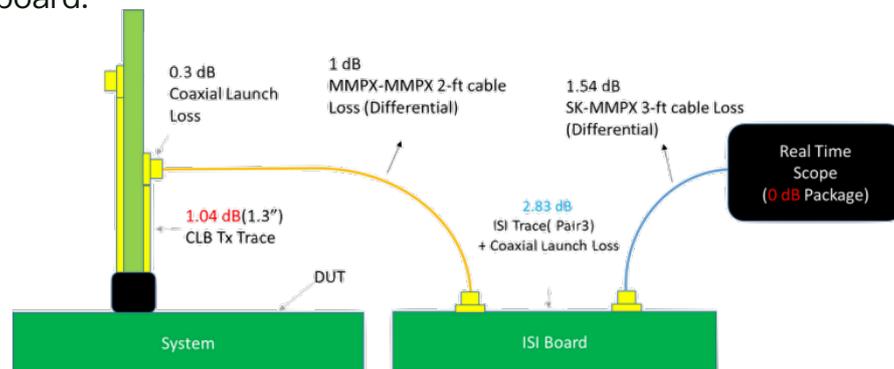
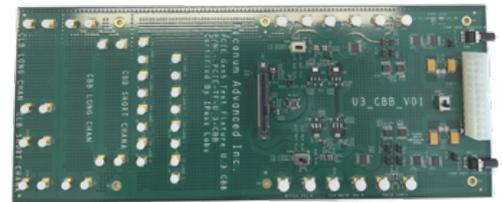
CLB

- Signal Integrity Board for U.2(Signal Integrity Board for U.3(SFF-TA- 1001) Physical Layer Conformance.
- Support PCIe5.0 (32GT/s) used for testing Transmitters.
- Paired with U.3 Gen5 (CBB) used for Receiver Calibration.
- Comes with one (1) CLB. PCIE-TF-U.3-CLB.
- Need additional Gen5 CEM (SIG-REV4) ISI Board for the testing.
- MMPX connector is 100% compatible with PCI-SIG CEM ISI board.



CBB

- Signal Integrity Board for U.3(SFF-TA- 1001) Physical Layer Conformance.
- Support PCIe5.0 (32GT/s) used for testing Receivers.
- Paired with U.2 Gen5 (CLB) used for Receiver Calibration.
- Paired with U.3 Gen5 (CLB) used for Receiver Calibration.
- Comes with one (1) CBB. PCIE5-TF-U.3- CBB.
- MMPX connector is 100% compatible with PCI-SIG CEM ISI board.



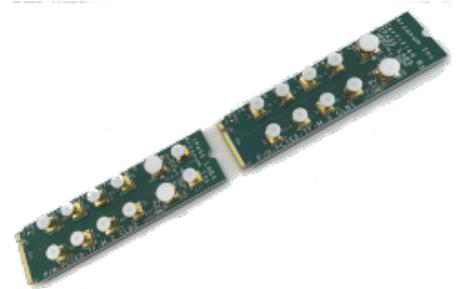
U.3 Loss

	CLB Tx Trace Loss	Coaxial Launch Loss(Pair)	MMPX-MMPX 2-ft Cable Loss	ISI Trace Loss	SK-MMPX 3-ft Cable Loss	Package Loss	Total Loss (Target: 6.5 dB)
Actual	1.04dB	0.3dB	1dB	2.83dB	1.54dB	0dB	6.71dB

PCIe Gen 5 M.2

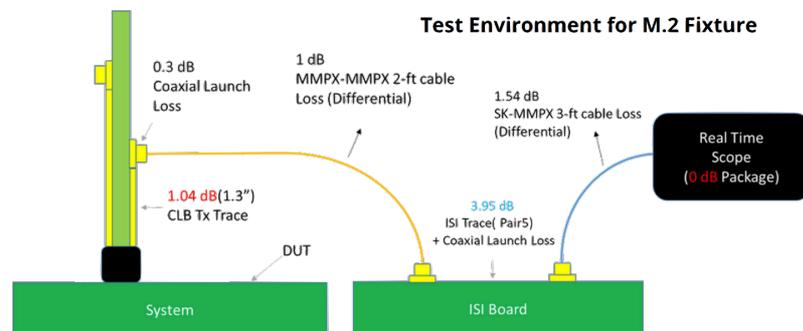
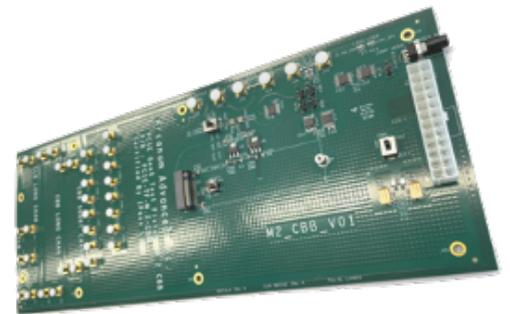
CLB

- Signal Integrity Board for M.2 Physical Layer Conformance.
- Support PCIe5.0 (32GT/s) used for testing Transmitters.
- Paired with M.2 Gen5 (CBB) used for Receiver Calibration
- Comes with two (2) CLBs. PCIE-TF-M.2-CLB1 for Lane0-1; PCIE5-TF-M.2- CLB2 for Lane2-3.
- Need additional Gen5 CEM (SIG-REV4) ISI Board for the testing.
- MMPX connector is 100% compatible with PCI-SIG CEM ISI board.



CBB

- Signal Integrity Board for U.3(SFF-TA- 1001) Physical Layer Conformance.
- Support PCIe5.0 (32GT/s) used for testing Receivers.
- Paired with U.2 Gen5 (CLB) used for Receiver Calibration.
- Paired with U.3 Gen5 (CLB) used for Receiver Calibration.
- Comes with one (1) CBB. PCIE5-TF-U.3- CBB.
- MMPX connector is 100% compatible with PCI-SIG CEM ISI board.



M.2 Loss

	CLB Tx Trace Loss	Coaxial Launch Loss(Pair)	MMPX-MMPX 2-ft Cable Loss	ISI Trace Loss	SK-MMPX 3-ft Cable Loss	Package Loss	Total Loss (Target: 7.5 dB)
Actual	1.04dB	0.3dB	1dB	3.95dB	1.54dB	0dB	7.83dB

PCIe Gen 5 EDSFF (E1.S/E1.L)

CLB

- Signal Integrity Board for EDSFF(SFF-TA-1006, SFF-TA-1007) Physical Layer Conformance
- Support PCIe5.0 (32GT/s) used for testing Transmitters.
- Paired with EDSFF Gen5 (CBB) used for Receiver Calibration.
- Comes with one (1) CLB. PCIE-TF-EDSFF-CLB.
- Need additional Gen5 CEM (SIG-REV4) ISI Board for the testing.
- MMPX connector is 100% compatible with PCI-SIG CEM ISI board.

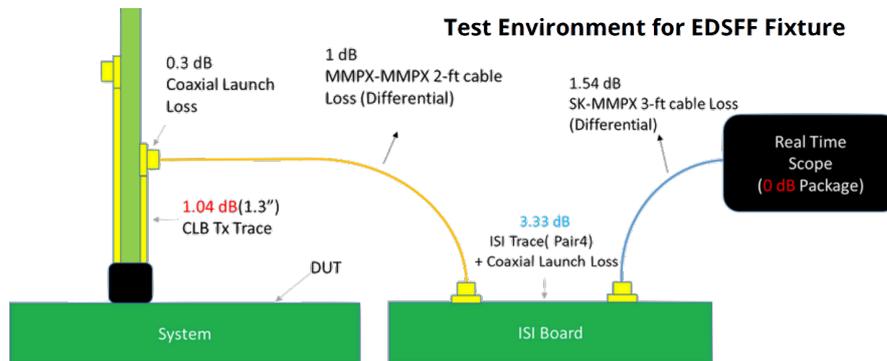
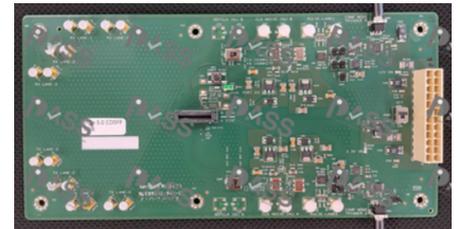


CBB

Signal Integrity Board for EDSFF(SFF-TA-1006, SFF-TA-1007) Physical Layer Conformance Support PCIe5.0 (32GT/s) used for testing Receivers.

Paired with EDSFF Gen5 (CLB) used for Receiver Calibration.

Comes with one (1) CBB. PCIE5-TF-EDSFF-CBB. MMPX connector is 100% compatible with PCI-SIG CEM ISI board.



EDSSF Loss

	CLB Tx Trace Loss	Coaxial Launch Loss(Pair)	MMPX-MMPX 2-ft Cable Loss	ISI Trace Loss	SK-MMPX 3-ft Cable Loss	Package Loss	Total Loss (Target: 7 dB)
Actual	1.04dB	0.3dB	1dB	3.33dB	1.54dB	0dB	7.21dB

PCIe Gen 5 OCP 3.0

CLB

- Signal Integrity Board for OCP3.0 NIC Physical Layer Conformance.
- Support PCIe5.0 (32GT/s) used for testing Transmitters.
- Paired with OCP3.0 Gen5 (CBB) used for Receiver Calibration.
- Comes with two (2) CLBs. PCIE5-TF-OCP30-CLB1 for Lane0-3 & Lane8-11; PCIE5-TF-OCP30-CLB2 for Lane4-7 & Lane8-15.
- Need additional Gen5 CEM (SIG-REV4) ISI Board for the testing.
- MMPX connector is 100% compatible with PCI-SIG CEM ISI board.



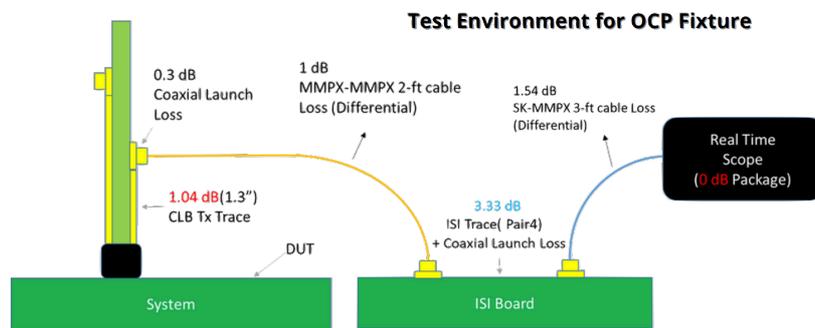
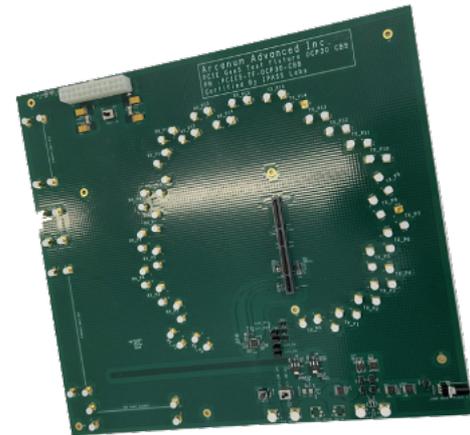
CBB

Signal Integrity Board for OCP3.0 NIC Physical Layer Conformance.

Support PCIe5.0 (32GT/s) used for testing Receivers.

Paired with OCP3.0 Gen5 (CLB) used for Receiver Calibration.

Comes with one (1) CBB. PCIE5-TF-OCP30-CBB. MMPX connector is 100% compatible with PCI-SIG CEM ISI board.



OCP 3.0 Loss

	CLB Tx Trace Loss	Coaxial Launch Loss(Pair)	MMPX-MMPX 2-ft Cable Loss	ISI Trace Loss	SK-MMPX 3-ft Cable Loss	Package Loss	Total Loss (Target: 7 dB)
Actual	1.04dB	0.3dB	1dB	3.33dB	1.54dB	0dB	7.21dB